

PATENT
Docket No. 29273/566

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventors : T. NISHIMUKAI et al.
Serial No. : (Con. of 09/886,267)
Filed : (Herewith)
For : DATA PROCESSOR

COMMISSIONER FOR PATENTS
P. O. Box 1450
Alexandria, Virginia 22313-1450

INFORMATION DISCLOSURE STATEMENT
UNDER 37 C.F.R. § 1.97 & § 1.98


S I R:

In conformance with Applicants' duty of disclosure under 37 C.F.R. § 1.56 and § 1.97(b)(1), the references listed on the attached form PTO-1449 are hereby brought to the Examiner's attention.

Since all of the references have already been considered by the Examiner in parent application Serial No. 09/886,267, no copies of the the references are required.

It is respectfully requested that the information be expressly considered during the prosecution of this application, and that the references be made of record therein and appear in the "references cited" on any patent to issue therefrom.

Respectfully submitted,



John C. Altmiller
(Reg. No. 25,951)

Date: 11 March 2004

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FORM PTO-1449
INFORMATION DISCLOSURE
STATEMENT BY APPLICANT(S)

Atty Docket No. : 29273/566
 Serial No. : (Con. of 09/886,267)
 Inventors : T. NISHIMUKAI et al.
 Filed :
 Group Art Unit : 2183 (Anticipated)
 Examiner : D. P. Pan (Anticipated)

U.S. PATENT DOCUMENTS

<u>Examiner Initial</u>	<u>Patent Number</u>	<u>Patent Date</u>	<u>Name</u>	<u>Class/ Subclass</u>	<u>Filing Date</u>
	4,989,140	01/91	NISHIMUKAI et al.	364/200	
	4,775,955	10/88	LIU	395/425	10/85
	4,740,889	04/88	MOTERSOLE et al.	364/200	06/26/84
	4,719,568	01/88	CARRUBBA et al.	364/200	
	4,713,755	12/87	WORLEY, Jr. et al.	395/375	06/85
	4,713,752	12/87	TONE	364/200	12/84
	4,707,784	11/87	RYAN et al.	364/200	
	4,701,844	10/87	THOMPSON et al.	364/200	
	4,675,806	06/87	UCHIDA	364/200	03/03/82
	4,646,233	02/87	WEATHERFORD	364/200	
	4,608,633	08/86	BOOTHROYD et al.	364/200	04/01/83
	4,602,368	07/86	CIRCELLO et al	364/900	04/15/83
	4,551,798	11/85	HORVATH	364/200	
	4,551,799	11/85	RYAN et al.	364/200	
	4,530,050	07/85	FUKUNAGA et al.	364/200	08/82
	4,521,851	06/85	TRUBISKY et al.	364/200	10/13/82
	4,521,850	06/85	WILHITE et al.	364/200	
	4,502,110	02/85	SAITO	364/200	
	4,481,573	11/84	FUKUNAGA et al.	364/200	
	4,471,432	09/84	WILHITE et al.	364/200	

EXAMINER

DATE CONSIDERED

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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	4,471,429	09/84	PORTER et al.	364/200	
	4,442,488	04/84	HALL	364/200	
	4,439,829	03/84	TSIANG	364/200	
	4,456,954	06/84	BULLIONS, III et al.	395/400	
	4,342,078	07/82	TREDENNICK et al.	364/200	
	4,332,010	05/82	MESSINA ET AL.	711/3	
	4,325,121	04/82	GUNTER et al.	364/200	
	4,322,795	03/82	LANGE et al.	395/425	
	4,312,034	01/82	GUNTER et al.	364/200	
	4,295,193	10/81	POMERENE	395/375	
	4,274,953	04/81	DOUGLAS et al.	209/326	
	4,179,736	12/79	WILHITE et al.	364/200	
	4,011,547	03/77	KIMMEL	364/200	
	3,979,726	09/76	LANGE et al.	364/200	
	3,859,636	01/75	COOK	364/200	
	3,845,474	10/74	LANGE et al.	364/200	
	3,781,823	12/73	SENESE	364/200	
	3,618,041	11/71	HORIKOSHI et al.	364/200	
	4,439,829	03/84	TSIANG	711/118	
	4,441,155	04/84	FLETCHER et al.	711/3	

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FOREIGN PATENT DOCUMENTS

Examiner Initial	Document Number	Date	Country	Class/ Subclass	Translation Yes No
	50-138738	11/05/75	Japan		ABS.
	57-15494	06/30/80	Japan		X

OTHER DOCUMENTS

(Including Author, Title, Date, Pertinent Pages, Etc.)

Examiner
Initial

- | | | |
|-------|----|---|
| _____ | 1. | <i>Integrated MMU, Cache/Raise System-Level Issues</i> , Sorin Lacobovici et al., Computer Design, Vol. 26, No. 10, May 15, 1987, pp. 75-79. |
| | 2. | <i>MC68020 32-BIT MICROPROCESSOR USER'S MANUAL</i> , Second Edition, Englewood Cliffs, NJ, PRENTICE HALL, INC., 1984, PP. 1-2, 1-9/1-10, 6-8, 6-9, 7-1 TO 7-4. |
| _____ | 3. | Computing Survey, Vol. 14, No. 3, Sep. 3, 1982, pp. 473-530, A.J. Smith: <i>Cache Memories</i> . |
| _____ | 4. | Electronics International, Vol. 55, No. 16, Aug. 1982, pp. 112-117, Per Knudsen: <i>Supermini Goes Microprocessor Route to Put it Up Front in Performance</i> . |

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(Including Author, Title, Date, Pertinent Pages, Etc.)

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- _____ 5. Computer Design, Vol. 24, No. 3, Mar. 1985, pp. 173-181, T. Fossum et al.: *New VAX Squeezes Mainframe Power Into Mini Package.*
- _____ 6. "COMPTON 84", 28th IEEE Computer Society International Conference, digest of papers, *Intellectual Leverage The Driving Technologies*, Feb. 27, 1984, pp. 230-236, David Stevenson, Z80,000 32-Bit Microprocessor.
- _____ 7. Microcomputer Hardware, *The Iwanami Microelectronics Course*, Nov. 1984, Japan, pp. 144-145.
- _____ 8. *A Cache Memory for the NS16032 Virtual Memory Memory Processor*, R.S. Lim, 1984, IEEE, pp. 377-381.

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